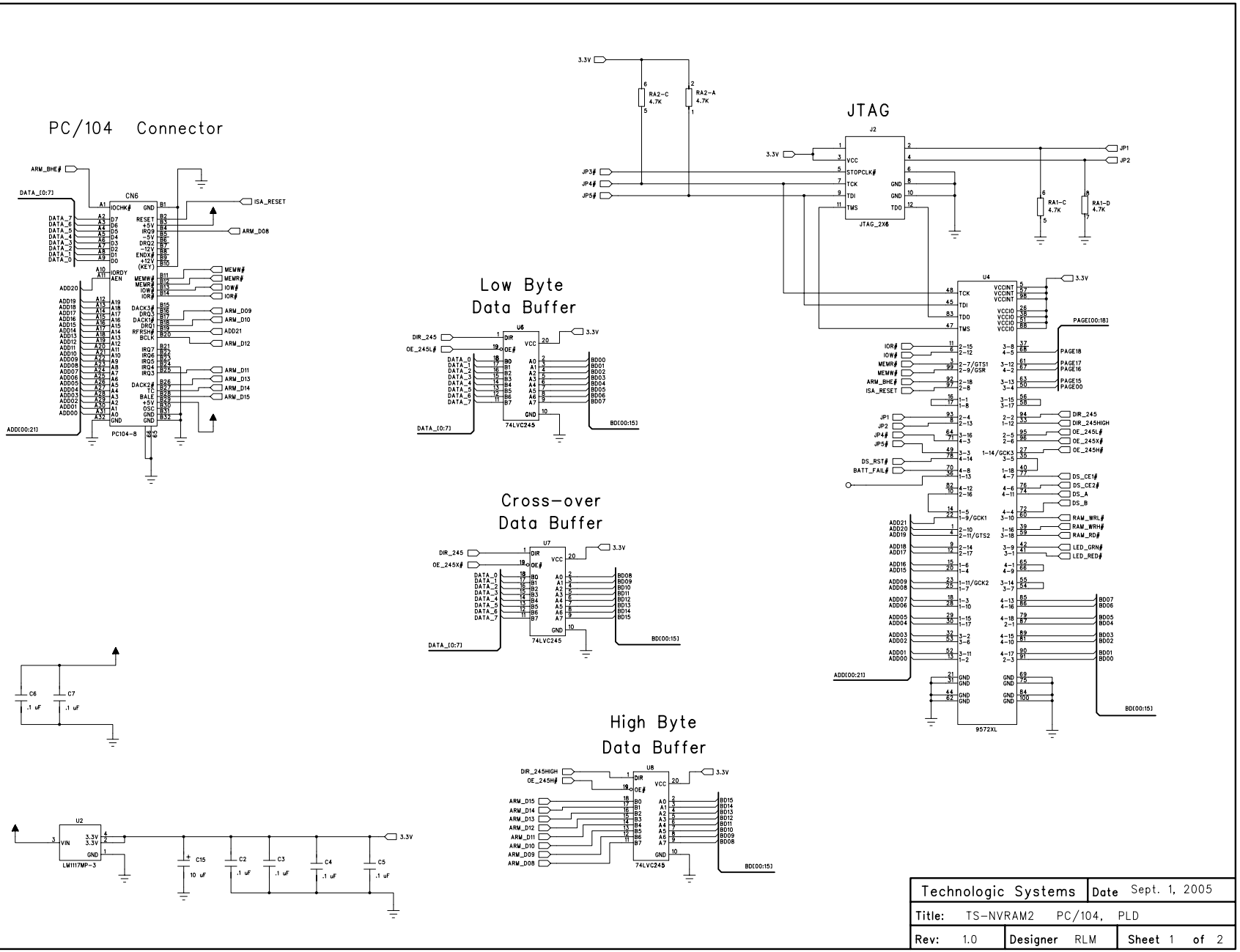
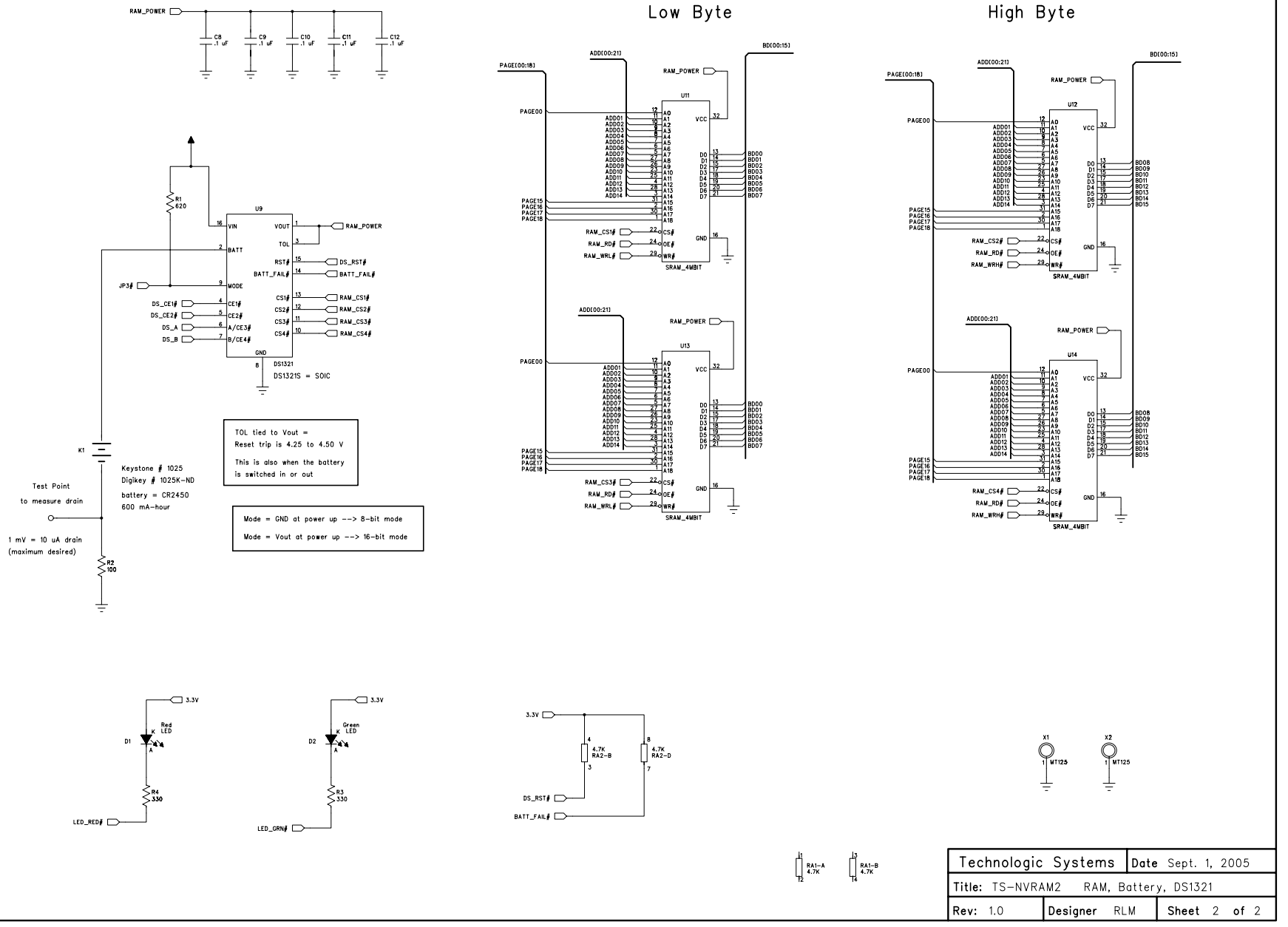

PC/104 Connector
JTAG
Low Byte
Data Buffer
Cross-over
Data Buffer
High Byte
Data Buffer
Technologic Systems
Date Sept. 1, 2005
Title: TS-NVRAM2 PC/104, PLD
Rev: 1.0
Designer RLM
Sheet 1 of 2

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|---|---|---|
| Technologic Systems | Date Sept. 1, 2005 | |
| Title: TS-NVRAM2 RAM, Battery, DS1321 | | |
| Rev: 1.0 | Designer RLM | Sheet 2 of 2 |